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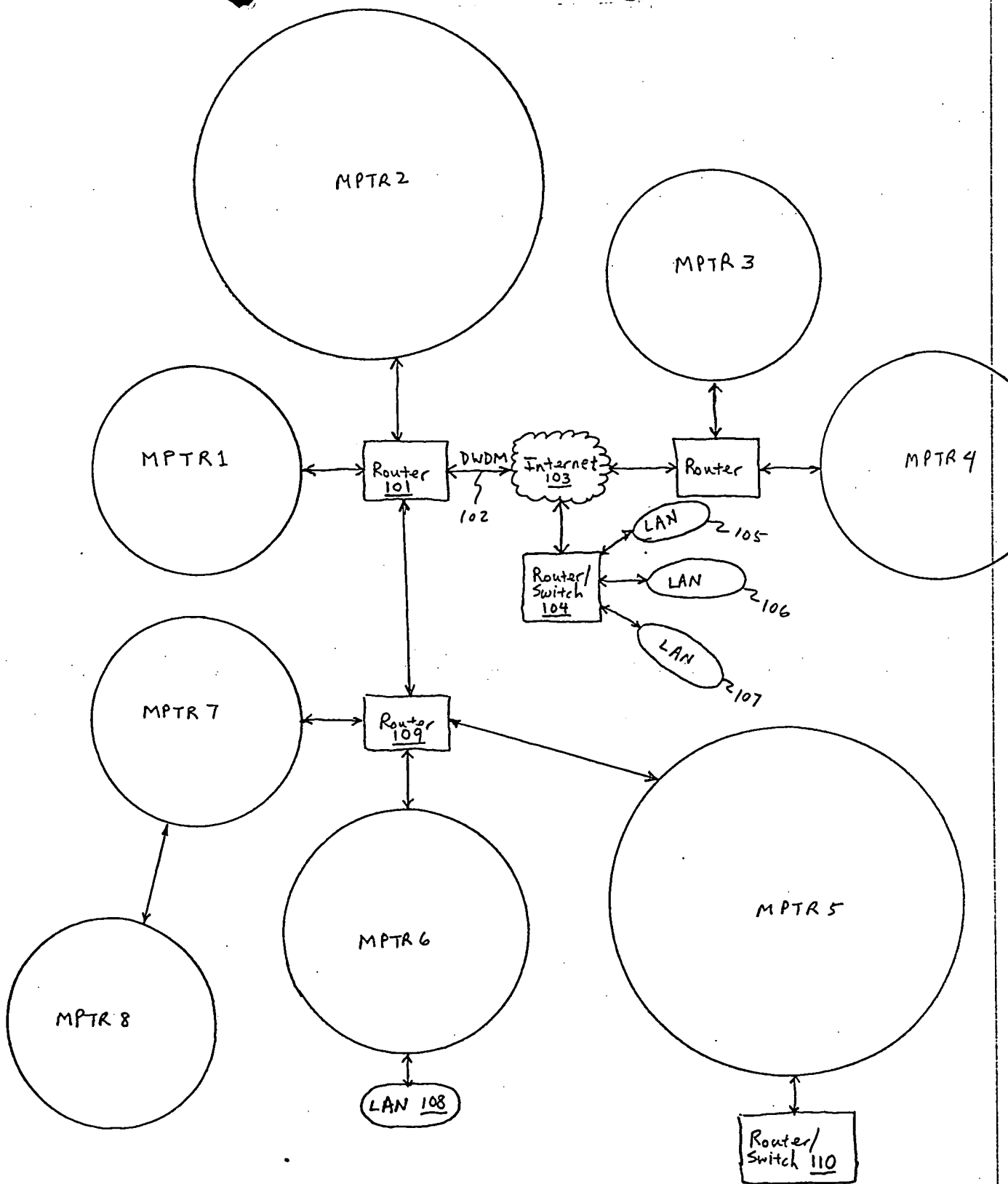


Figure 1

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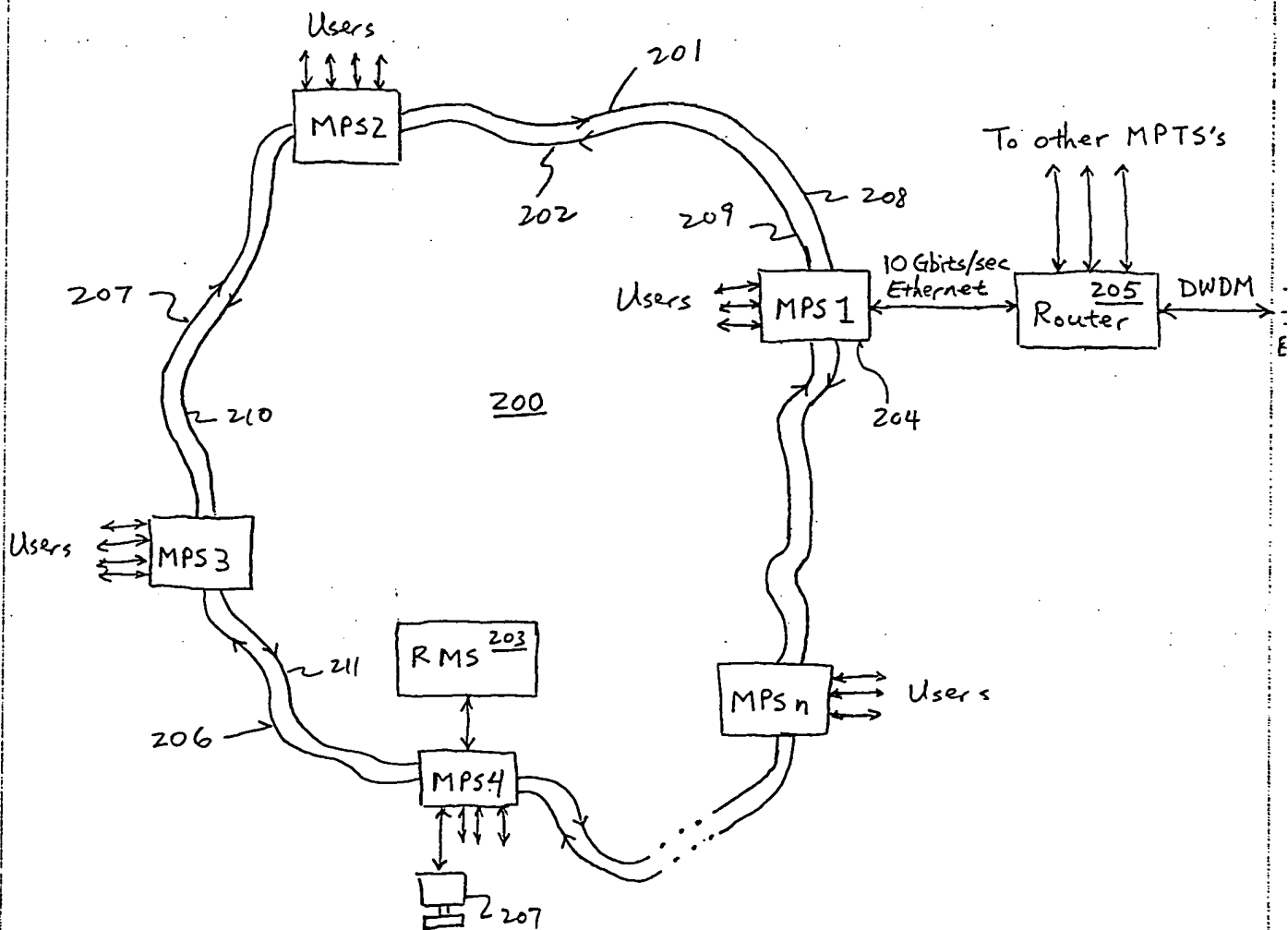


Figure 2

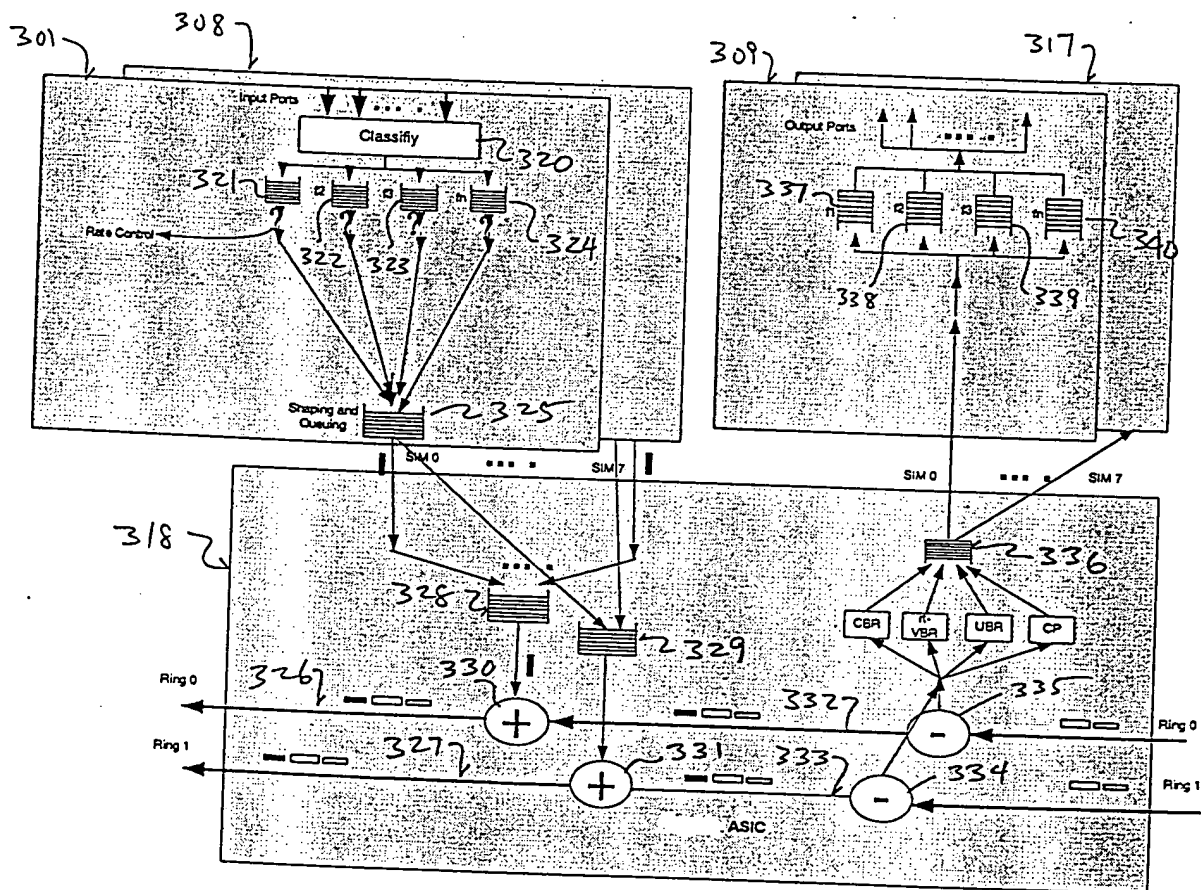


Figure 3

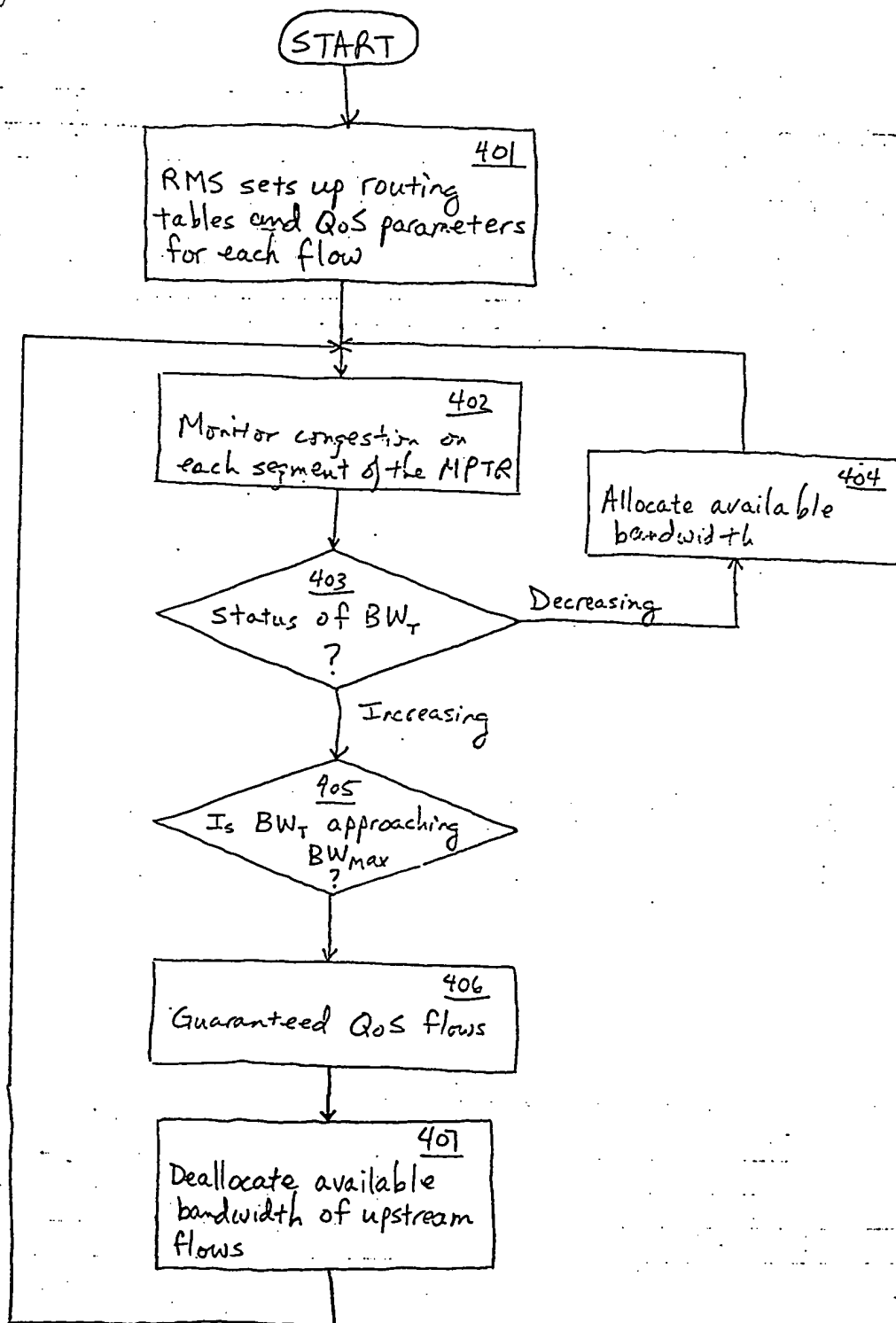
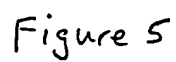


Figure 4

A block diagram of a parallel processing system. At the top, three input sources labeled 508, 509, and 510 feed into a horizontal bus. Below this bus are five vertical processing units, each consisting of a stack of five rectangular blocks. The units are labeled 531, 532, 533, 534, and 535 from left to right. Each unit has a control line (544, 545, 546, 547, 548) that passes through a switch symbol before entering a central circular adder labeled 'MPS' with a '+' sign. The output of the adder is labeled 525. A feedback line labeled 502 originates from the output of the adder and loops back to the input of the first unit (531).



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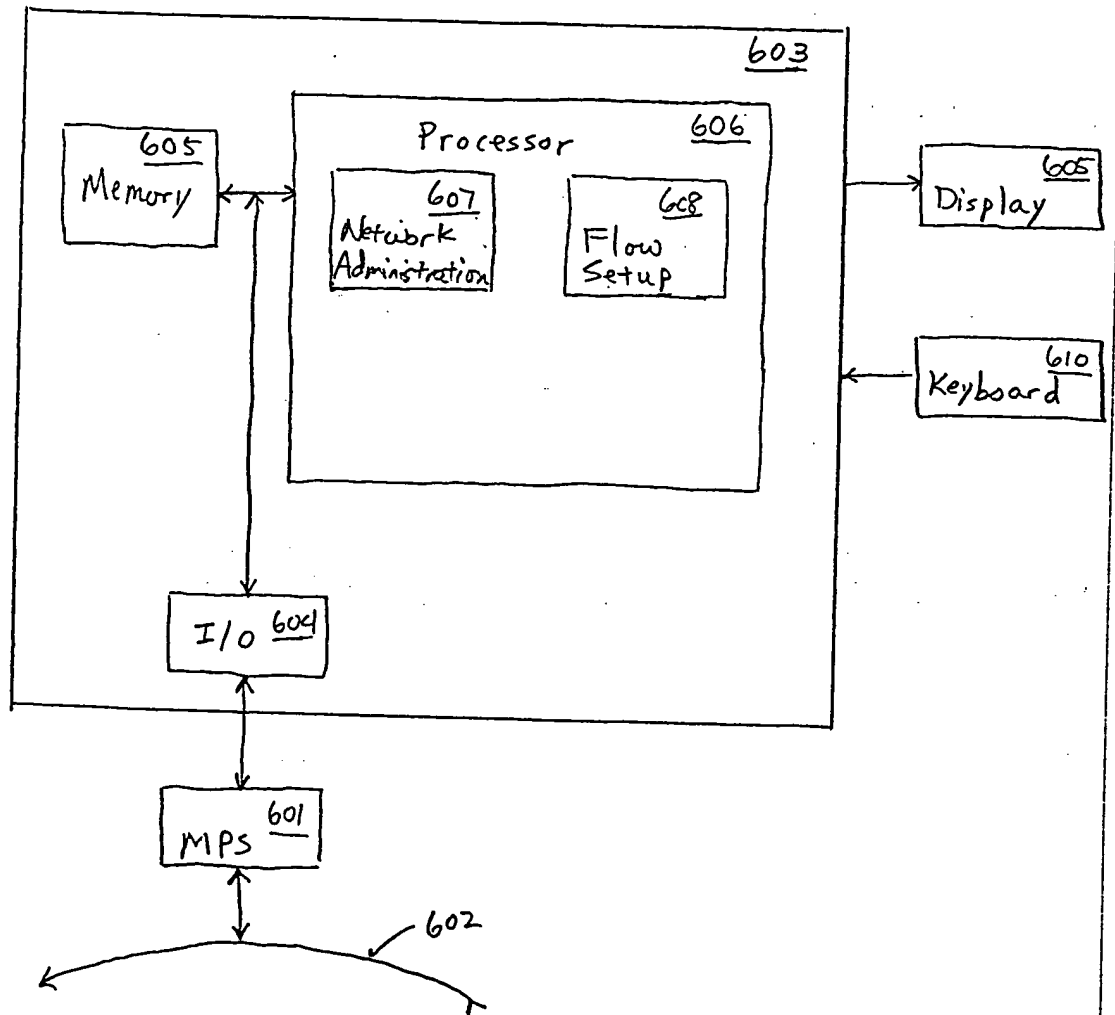


Figure 6.